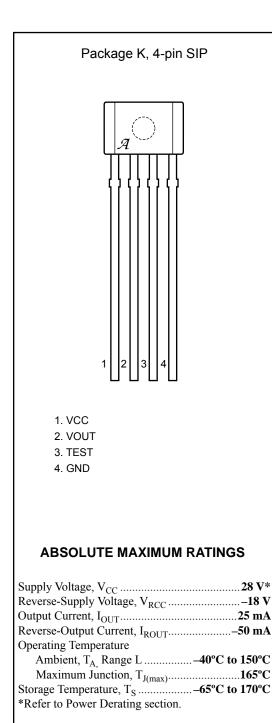
High Precision Hall Effect AC-Coupled Differential Sensor with Integrated Filter Capacitor



The A1421, A1422 and A1423 are ac-coupled Hall-effect sensors which include monolithic integrated circuits that switch in response to changing differential magnetic fields created by rotating ring magnets or, when coupled with a magnet, by ferrous targets. This family of devices also includes an integrated capacitor that provides the high accuracy of analog sensing without an external filter capacitor. This reduces cost and components, while improving the reliability of the final sensor solution.

Magnetic field changes are sensed by two integrated Hall transducers and then are differentially amplified on the chip. Differential sensing provides immunity to radial vibration, within the device operating air gap range, by rejection of this common-mode signal change. Steady-state system offsets are eliminated using an on-chip differential bandpass filter with integrated capacitor. This filter also provides relative immunity to interference from electromagnetic sources. The device utilizes advanced temperature compensation for the high-pass filter, sensitivity, and Schmitt trigger switchpoints to guarantee optimal operation to low frequencies over a wide range of air gaps and temperatures.

Each device includes: a voltage regulator, two Hall transducers, temperature compensating circuitry, a low-level amplifier, bandpass filter, Schmitt trigger, and an output driver. The on-board regulator permits operation with supply voltages from 4.0 to 26.5 V. The output stage can switch 20 mA over the full frequency response range of the sensor, and is compatible with TTL and CMOS logic circuits.

Continued on next page...

Features and Benefits

- Integrated tracking capacitor
- Senses motion of ring magnet or ferrous targets
- Wide operating temperature range
- Operation with magnetic input signal frequency from 20 Hz to 30 kHz
- EMI/ESD-resistant
- Large effective air gaps
- 4.0 to 26.5 V supply operating range
- Output compatible with both TTL and CMOS logic families
- Reverse battery protection
- Resistant to mechanical and thermal stress
- Accurate true zero crossing switchpoint (A1421 only)
- High vibration immunity, in running mode (A1423 only)





High Precision Hall Effect AC-Coupled Differential Sensor with Integrated Filter Capacitor

The devices in this family differ from each other in their switchpoint speci? cations and their switching polarity. The A1421 has a small hysteresis and asymmetrical switchpoints, with one switchpoint at the zero-crossing. The A1422 has a small hysteresis and symmetrical switchpoints, both near the zerocrossing. The A1423 offers high vibration immunity, by means of its larger hysteresis that establishes symmetrical switchpoints further from the zero-crossing. The output polarities are shown in the Product Selection Guide table. This variety of options provides ? exibility for achieving solutions for a wide range of applications, including automotive transmission and crankshaft speed sensing.

The device package has an operating ambient temperature range -40 °C to 150°C, and is provided in a 4-pin plastic SIP. Each package is available in a lead (Pb) free version (suffix, -T), with a 100% matte tin plated leadframe.

Product Selection Guide

		Output Switching at B _{DIFF} = 0		Switchpoints		Symmetry				
Part Number Pb-free ¹		B _{Diff} Increasing	B _{Diff} Decreasing	B _{OP(typ)} (G)	B _{RP(typ)} (G)	B _{OP(max)} + B _{RP(min)} (G)	B _{OP(typ)} + B _{RP(typ)} (G)	B _{OP(min)} + B _{RP(max)} (G)	Packing2	
A1421LK-T	Yes	Low (On) to High (Off)	High (Off) to Low (On)	15	0	15	15	7.5	Bulk, 500 pieces/bag	
A1422LK-T	Yes	High (Off) to Low (On)	Low (On) to High (Off)	15	-15	0	0	0		
A1423LK A1423LK-T	– Yes	High (Off) to Low (On)	Low (On) to High (Off)	65	-65	0	0	0		

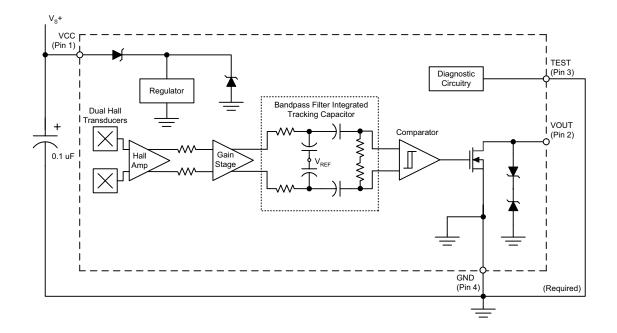
¹Pb-based variants are being phased out of the product line. Certain variants cited in this footnote are in production but have been determined to be NOT FOR NEW DESIGN. This classification indicates that sale of this device is currently restricted to existing customer applications. The variants should not be purchased for new design applications because obsolescence in the near future is probable. Samples are no longer available. Status change: May 1, 2006. These variants include: A1421LK and A1422LK.

²Contact Allegro for additional packing options.



High Precision Hall Effect AC-Coupled Differential Sensor with Integrated Filter Capacitor

Functional Block Diagram





High Precision Hall Effect AC-Coupled Differential Sensor with Integrated Filter Capacitor

OPERATING CHARACTERISTICS Valid at $T_A = -40^{\circ}$ C to 150°C, $T_J \le 165^{\circ}$ C; over operational air gap range and V_{CC} within operating range, unless otherwise noted. Typical operating parameters: $V_{CC} = 12$ V and $T_A = 25^{\circ}$ C.

Characteristic	Symbol	Test Conditions	Min.	Тур.	Max.	Units
ELECTRICAL CHARACTERISTICS				1		
Supply Voltage	V _{CC}	Operating; T _J < T _{J(max)}	4.0	12	26.5	V
Supply Current	I _{CC}		_	4.2	7.0	mA
Output Saturation Voltage	V _{OUT(SAT)}	I _{SINK} = 20 mA	-	140	400	mV
Output Leakage Current	I _{OFF}	V _{OUT} = 24 V, B _{diff} = 0	-	_	5	μA
PROTECTION COMPONENT CHAP	ACTERISTI	cs		•		
Reverse Supply Current	I _{RCC}	V _{CC} = -18 V	-	_	_1	mA
Supply Zener Current	I _{ZSupply}	V _S = 28 V	-	_	10	mA
Supply Zener Clamp Voltage ¹	V _{ZSupply}	I _{CC} = 10 mA, T _A = 25°C	28	33	37	V
Output Zener Current	I _{ZOutput}	V _{OUT} = 28 V	-	_	3	mA
Output Zener Clamp Voltage	V _{ZOutput}	I _{OUT} = 3 mA, T _A = 25°C	28	_	-	V
Output Short Circuit Current Limit	I _{OUTS(lim)}		_	_	50	mA
RESPONSE CHARACTERISTICS					1	<u> </u>
Power-On State	POS	t < t _{Response}	-	High	-	V
Power-On Time ^{2,6}	t _{PO}	$V_{CC} > V_{CC(min)}$	-	4.5	9	ms
Settling Time ^{3,6}	t _{Settling}	f _{Bdiff} ≥ 100 Hz	0	_	50	ms
Response Time ⁶	t _{Response}	Equal to t _{PO} + t _{Settling} ; f _{Bdiff} ≥ 100 Hz	4.5	_	59	ms
Upper Corner Frequency	f _{CU}	–3 dB, single pole	20	_	-	kHz
Lower Corner Frequency	f _{CL}	-3 dB, single pole	_	_	20	Hz
OUTPUT CHARACTERISTICS		•	·			·
Output Rise Time ⁴	t _r	R _{PU} = 1 kΩ, C _{OUTC2} = 10 pF	-	_	200	ns
Output Fall Time	t _f	R _{PU} = 1 kΩ, I _{SINK} = 20 mA, C _{OUTC2} = 10 pF	-	_	200	ns

Continued on next page.



High Precision Hall Effect AC-Coupled Differential Sensor with Integrated Filter Capacitor

OPERATING CHARACTERISTICS, continued Valid at T_A = −40°C to 150°C, T_J≤165°C; over operational air gap range and V_{CC} within operating range, unless otherwise noted. Typical operating parameters: V_{CC} = 12 V and T_A = 25°C.

Characteristic	Symbol	Test Conditions	Min.	Тур.	Max.	Units
MAGNETIC CHARACTE	RISTICS ^{5,6}					<u> </u>
Operate Point		1421, B_{diff} increasing, f_{Bdiff} = 200 Hz, B_{diff} = 50 Gp-p V _{OUT} switches from low (on) to high (off)	0.0	15.0	27.5	G
	B _{OP}	1422, B_{diff} increasing, f_{Bdiff} = 200 Hz, B_{diff} = 50 Gp-p V _{OUT} switches from high (off) to low (on)	5.0	15.0	35.0	G
		1423, B_{diff} increasing, f_{Bdiff} = 200 Hz, B_{diff} = 200 Gp-p V _{OUT} switches from high (off) to low (on)	10.0	65.0	100.0	G
Release Point		1421, B_{diff} decreasing, f_{Bdiff} = 200 Hz, B_{diff} = 50 Gp-p V_{OUT} switches from high (off) to low (on)	-12.5	0.0	7.5	G
	B _{RP}	1422, B_{diff} decreasing, f_{Bdiff} = 200 Hz, B_{diff} = 50 Gp-p V_{OUT} switches from low (on) to high (off)	-35.0	-15.0	-5.0	G
		1423, B_{diff} decreasing, f_{Bdiff} = 200 Hz, B_{diff} = 200 Gp-p V_{OUT} switches from low (on) to high (off)	-100.0	-65.0	-10.0	G
		1421, f _{Bdiff} = 200 Hz, B _{diff} = 50 Gp-p	5	15	35	G
Hysteresis	B _{HYS}	1422, f _{Bdiff} = 200 Hz, B _{diff} = 50 Gp-p	-	30	-	G
		1423, f _{Bdiff} = 200 Hz, B _{diff} = 200 Gp-p	-	130	27.5 0 35.0 0 100.0 0 7.5 0 -5.0 0 -10.0 0 35 0 - 0 - 0	G
Applied Magnetic Field ⁷	B _{diff}	Differential p-p magnetic field	-	-	1250	G

 $^{1}I_{CC}$ is equivalent to $I_{CC(max)}$ + 3 mA. ²Time required to initialize device.

³Time required for the output switchpoints to be within specification.

⁴Output Rise Time will be dominated by the RC time constant.

⁵For lower frequencies, the absolute values of B_{OP}, B_{RP}, and B_{HYS} may decrease due to delay induced by the high-pass filter.

⁶ See Definitions of Terms section.

⁷ Exceeding the maximum magnetic field may result in compromised absolute accuracy.

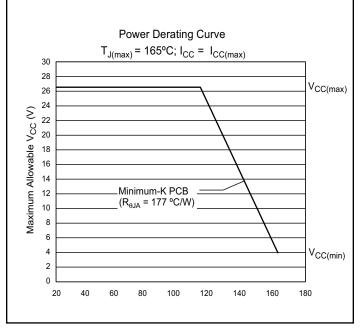


High Precision Hall Effect AC-Coupled Differential Sensor with Integrated Filter Capacitor

THERMAL CHARACTERISTICS may require derating at maximum conditions, see application information

Characteristic	Symbol	Test Conditions*	Value	Units
Package Thermal Resistance	$R_{\theta JA}$	Minimum-K PCB (single-sided with copper limited to solder pads)	177	°C/W

*In still air. Additional thermal information available on Allegro Web site.



Maximum Power Dissipation, P_{D(max)} $T_{J(max)} = 165^{\circ}C; V_{CC} = V_{CC(max)}; I_{CC} = I_{CC(max)}$ 900 850 800 750 700 650 Power Dissipation, P_D (mW) Minimum + PCB 600 (Reys 17) 550 500 °Cm 450 400 350 300 250 200 150 100 50 0 20 40 60 80 100 120 140 160 180 Temperature (°C)

Definitions of Terms

The following provide additional information about some of the parameters cited. For additional information, visit the Allegro Web site at *www.allegromicro.com*.

Applied Magnetic Field, B_{diff} – The differential magnetic flux density, which is calculated as the arithmetic difference of the flux densities observed by each of the two Hall elements. f_{Bdiff} is the input signal frequency.

Output Off Switchpoint (Operate Point), B_{OP} – The value of increasing differential magnetic flux density at which the device output switches from low to high (A1421) or high to low (A1422 and A1423).

Output On Switchpoint (Release Point), B_{RP} – The value of decreasing differential magnetic flux density at which the device output switches from high to low (A1421) or from low to high (A1422 and A1423).

Power-On Time, t_{PO} – The time needed by the device, after

power is applied, to initialize all circuitry necessary for proper operation.

Settling Time, $t_{Settling}$ – The time required by the device, after t_{PO} , and after a valid magnetic signal has been applied, to provide proper output transitions. Settling time is a function of magnetic offset, offset polarity, signal phase, signal frequency, and signal amplitude.

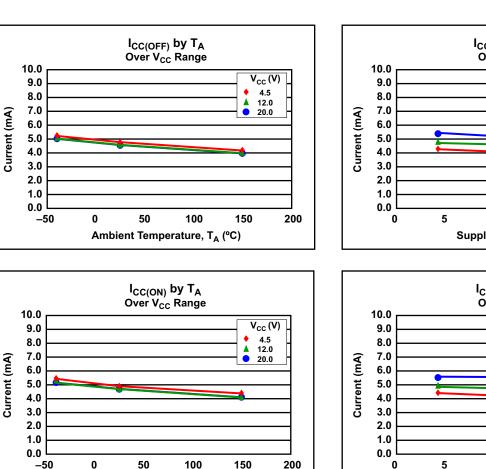
Supply Current (on), $I_{CC(on)}$ – The current draw of the device with the output transitor is turned on.

Supply Current (off), $I_{CC(off)}$ – The current draw of the device with the output transitor is turned off.

Response Time, t_{Response} – The total time required for generating zero-crossing output transitions after initialization (the sum of Power-on Time and Settling Time).



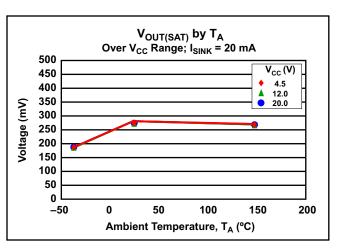
High Precision Hall Effect AC-Coupled Differential Sensor with Integrated Filter Capacitor



Empirical Results

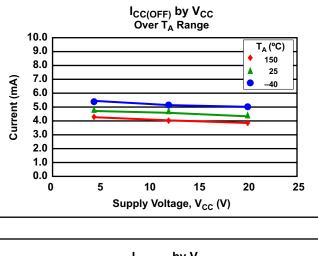
Allegro

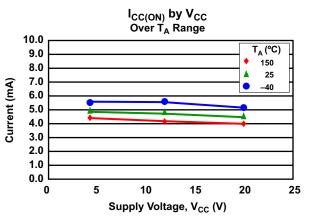
icroSystems Inc.

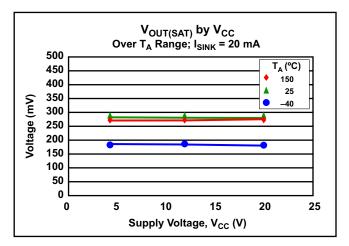


Ambient Temperature, T_A (°C)

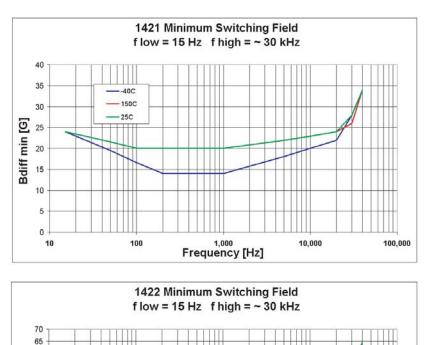
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High Precision Hall Effect AC-Coupled Differential Sensor with Integrated Filter Capacitor



60

55

50

10

Bdiff min [G]

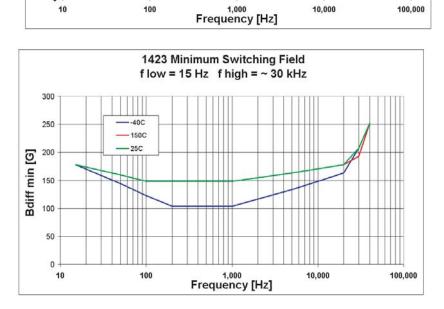
--40C -

- 150C

25C

100

Simulation Results



10,000

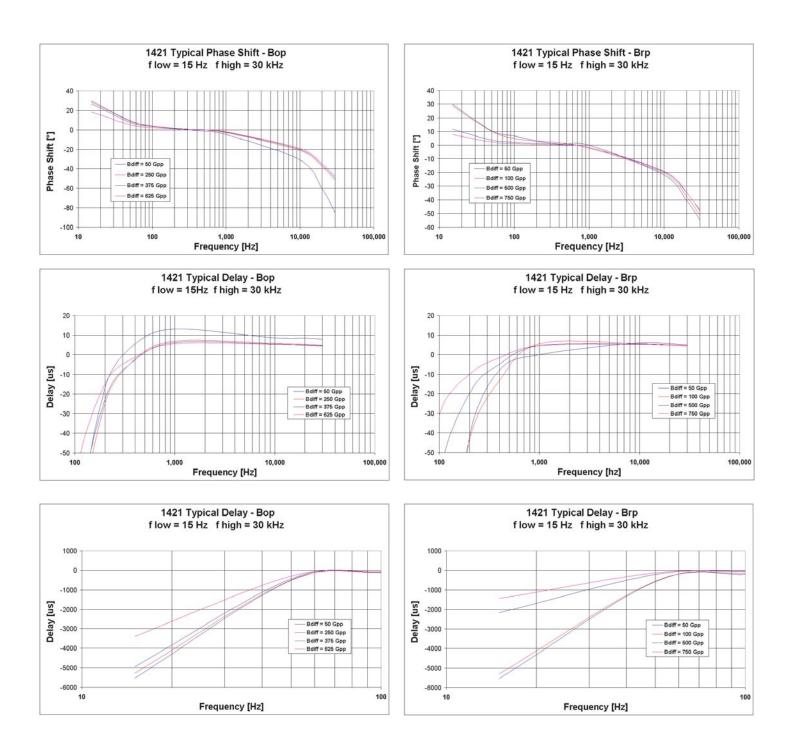
100,000

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High Precision Hall Effect AC-Coupled Differential Sensor with Integrated Filter Capacitor

Simulation Results, continued

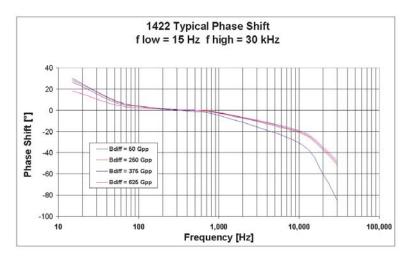


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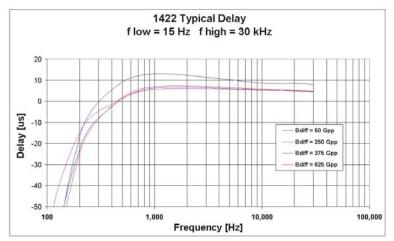


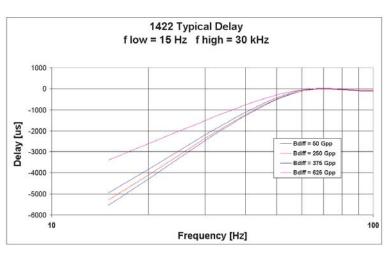
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High Precision Hall Effect AC-Coupled Differential Sensor with Integrated Filter Capacitor



Simulation Results, continued





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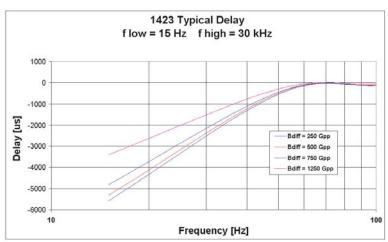


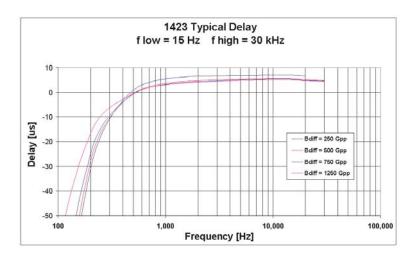
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High Precision Hall Effect AC-Coupled Differential Sensor with Integrated Filter Capacitor



Simulation Results, continued







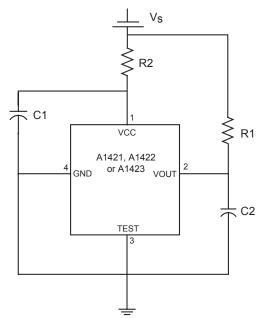
High Precision Hall Effect AC-Coupled Differential Sensor with Integrated Filter Capacitor

Sensor Evaluation: EMC Characterization

Please contact Allegro MicroSystems for EMC performance information. (EMC test results are available after review of first silicon.)

Test Name	Reference Specification			
ESD – Human Body Model*	AEC-Q100-002			
ESD – Machine Model	AEC-Q100-003			
Conducted Transients	ISO 7637-1			
Direct RF Injection	ISO 11452-7			
Bulk Current Injection	ISO 11452-4			
TEM Cell	ISO 11452-3			

*ESD test is done with no external components.



Component	Value	Units
R1*	1	kΩ
R2	100	Ω
C1	0.1	μF
C2	0.1	ηF

^{*}Pull-up resistor not required for protection but for normal operation.

Recommended EMC test circuit. Test circuit recommended configuration may change after evaluation of first silicon.



High Precision Hall Effect AC-Coupled Differential Sensor with Integrated Filter Capacitor

Applications Information

The A1421, A1422, and A1423 are versatile high-precision differential sensors that can be used in a wide range of applications. Proper choice of the target material and shape, magnet material and shape, and assembly techniques enables large working air gaps and high switchpoint accuracy over the device operating temperature range.

Sensor Operation

The device sensor IC contains two integrated Hall transducers that are used to differentially sense a magnetic field across the surface of the IC. Referring to figure 1, which shows curves for the A1421 as an example, the trigger switches the output when the differential magnetic field crosses the B_{OP} level while increasing in strength (referred to as the positive direction). In the example, the A1421 output voltage switches high (off), and switches the output low (on) when the differential magnetic field crosses B_{RP} while decreasing (the negative direction).

The operation is achieved through the use of two separate comparators. One comparator has a positive hysteresis, B_{HYS1} , and the other a negative hysteresis, B_{HYS2} . Therefore, one comparator switches at the B_{OP} crossing on an increasing differential signal and the other switches at the B_{RP} crossing on a decreasing differential signal. The hysteresis on each comparator precludes false switching on noise or target jitter.

The behavior is similar for the A1422 and the A1423. The switchpoints are as shown in the magnetic charactersitics table, and the output polarity is inverted. This is illustrated in figure 2, on the next page.

Start-up

During power-on time, t_{PO} , the output signal, V_{OUT} , is high. Beyond this time, if the applied magnetic field, B_{diff} , is smaller than B_{HYS} , the switching state and V_{OUT} polarity are indeterminate. V_{OUT} will be valid for $B_{diff} > B_{HYS}$, after the additional settling time, $t_{Settling}$, has also elapsed.

Delay

The bandpass filter induces delay in the output signal, $V_{\rm OUT}$, relative to the applied magnetic field, $B_{\rm diff}$. Simulation data shown

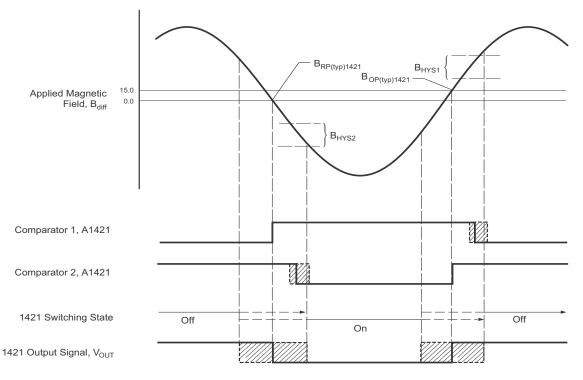


Figure 1. Typical output characteristics with dual comparator operation. The example shown is for the A1421. Characteristics shown without delay, see characteristic data charts for delay and phase shift contributions.



High Precision Hall Effect AC-Coupled Differential Sensor with Integrated Filter Capacitor

in the Characteristic Data section quantify the effect of the input signal amplitude on the phase shift of the output. Positive values of delay indicate a lagging output, while negative values indicate a leading output.

AC-Coupled Operation

Steady-state magnet and system offsets are eliminated using an on-chip differential bandpass filter. The upper and lower cut-off frequencies of this patented filter are set using an internal integrated capacitor. The differential structure of this filter improves the ability of the IC to reject single-ended noise on the GND or VCC lines and, as a result, makes the device more resistant to EMI (electromagnetic interference) typically seen in hostile remote-sensing environments.

Power Supply Protection

The device contains an on-chip voltage regulator and can operate over a wide supply voltage range. In applications that operate the device from an unregulated power supply, transient protection must be added externally. For applications using a regulated line, EMI/RFI protection may still be required. The circuit shown in figure 3 is the most basic configuration required for proper device operation.

Typical Circuit

A pull-up resistor, R_{PU} , is required between the supply and output terminals, as shown in figure 3. Also, the auxilliary terminal, TEST, must be connected externally to the GND terminal.

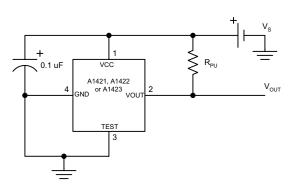


Figure 3. Basic application circuit. A pull-up resistor, $\mathsf{R}_{\mathsf{PU}},$ is required with the output driver.

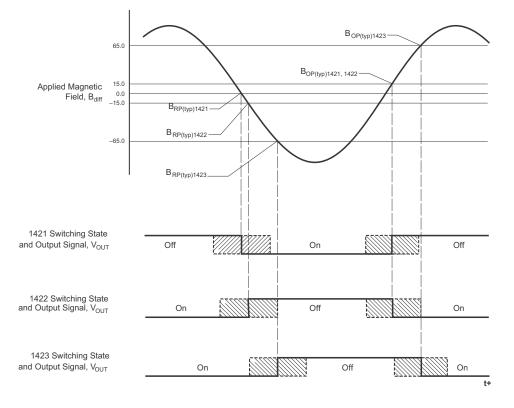


Figure 2. Comparative typical output characteristics. This chart illustrates the switchpoints and the output polarities of the A1421, A1422, and the A 1423. Characteristics shown without delay, see characteristic data charts for delay and phase shift contributions.



High Precision Hall Effect AC-Coupled Differential Sensor with Integrated Filter Capacitor

Power Derating

The device must be operated below the maximum junction temperature of the device, $T_{J(max)}$. Under certain combinations of peak conditions, reliable operation may require derating supplied power or improving the heat dissipation properties of the application. This section presents a procedure for correlating factors affecting operating T_J . (Thermal data is also available on the Allegro MicroSystems Web site.)

The Package Thermal Resistance, $R_{\theta JA}$, is a figure of merit summarizing the ability of the application and the device to dissipate heat from the junction (die), through all paths to the ambient air. Its primary component is the Effective Thermal Conductivity, K, of the printed circuit board, including adjacent devices and traces. Radiation from the die through the device case, $R_{\theta JC}$, is relatively small component of $R_{\theta JA}$. Ambient air temperature, T_A , and air motion are significant external factors, damped by overmolding.

The effect of varying power levels (Power Dissipation, P_D), can be estimated. The following formulas represent the fundamental relationships used to estimate T_J , at P_D .

$$\begin{split} P_D &= V_{IN} \times I_{IN} \quad (1) \\ \Delta T &= P_D \times R_{\theta JA} \quad (2) \\ T_J &= T_A + \Delta T \quad (3) \end{split}$$

For example, given common conditions such as: $T_A = 25$ °C, $V_{CC} = 12$ V, $I_{CC} = 4.2$ mA, and $R_{\theta JA} = 177$ °C/W, then:

 $P_D = V_{CC} \times I_{CC} = 12 \text{ V} \times 4.2 \text{ mA} = 50 \text{ mW}$ $\Delta T = P_D \times R_{\theta JA} = 50 \text{ mW} \times 177 \text{ °C/W} = 9 \text{ °C}$ $T_1 = T_A + \Delta T = 25 \text{ °C} + 9 \text{ °C} = 34 \text{ °C}$

A worst-case estimate, $P_{D(max)}$, represents the maximum allowable power level ($V_{CC(max)}$, $I_{CC(max)}$), without exceeding $T_{J(max)}$, at a selected $R_{\theta JA}$ and T_A .

Example

Reliability for V_{CC} at T_A=150°C, package L-I1, using minimum-K PCB

Observe the worst-case ratings for the device, specifically: $R_{\theta JA}=177^{\circ}C/W$, $T_{J(max)}=165^{\circ}C$, $V_{CC(max)}=26.5$ V, and $I_{CC(max)}=7.0$ mA.

Calculate the maximum allowable power level, $P_{D(max)}$. First, invert equation 3:

$$\Delta T_{max} = T_{J(max)} - T_A = 165 \circ C - 150 \circ C = 15 \circ C$$

This provides the allowable increase to T_J resulting from internal power dissipation. Then, invert equation 2:

$$P_{D(max)} = \Delta T_{max} \div R_{\theta JA} = 15^{\circ}C \div 177^{\circ}C/W = 91 \text{ mW}$$

Finally, invert equation 1 with respect to voltage:

$$V_{CC(est)} = P_{D(max)} \div I_{CC(max)} = 91 \text{ mW} \div 7.0 \text{ mA} = 13 \text{ V}$$

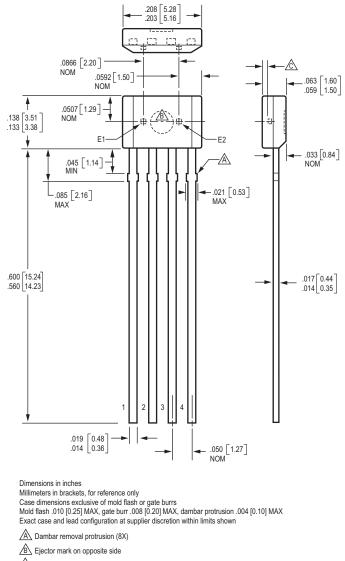
The result indicates that, at T_A , the application and device can dissipate adequate amounts of heat at voltages $\leq V_{CC(est)}$.

Compare $V_{CC(est)}$ to $V_{CC(max)}$. If $V_{CC(est)} \leq V_{CC(max)}$, then reliable operation between $V_{CC(est)}$ and $V_{CC(max)}$ requires enhanced $R_{\theta JA}$. If $V_{CC(est)} \geq V_{CC(max)}$, then operation between $V_{CC(est)}$ and $V_{CC(max)}$ is reliable under these conditions.



High Precision Hall Effect AC-Coupled Differential Sensor with Integrated Filter Capacitor

Package K, 4-pin SIP



Active Area Depth .0165 [0.42] NOM

The products described herein are manufactured under one or more of the following U.S. patents: 5,045,920; 5,264,783; 5,442,283; 5,389,889; 5,581,179; 5,517,112; 5,619,137; 5,621,319; 5,650,719; 5,686,894; 5,694,038; 5,729,130; 5,917,320; and other patents pending.

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